COMBINED DECLARATION AND POWER OF ATTORNEY

(宣誓書及び委任状)

As a below named inventor, I hereby declare that:

the specification of which: (check one) [] is attached hereto.

My residence, post office address and citizenship are as stated next to my name. I believe I am the original, first and sole inventor (if only one name is listed below), or an original, first and joint inventor (if plural names are listed below), of the subject matter claimed and for which a patent is sought on the invention entitled:

PROCESS FOR MOUNTING ELECTRONIC DEVICE AND SEMICONDUCTOR DEVICE

	[X] was fi	led onMarch 26, 1998			
		plication Serial No.			
	and w	ras amended on			
Accounts of the control of the contr		(if applicab	le)		
The claims, as amended by any a I acknowledge the duty Code of Federal Regulations, Se I hereby claim foreign papplication (s) for patent or investigation of the patent or investigation of the patent or investigation.	to disclose information 1.56. riority benefits unde entor's certificate list te or any PCT interny me on the same su	erstand the contents of the above ideo above. Ion material to examination of this er Title 35, United States Code, Selected below and have also identified national application(s) designing at abject matter having a filing date examination.	application of tion 119 of below any for least one co	according to Ti any foreign oreign applicati untry other tha	itle 37, on(s)
Palor Foreign Application(s)	٠.		Priority	Claimed	
9-75970	Japan	27/March/1997	[x]	[]	
(Number)	(Country)	(Day/Month/Year Filed)	Yes	No	
(Number)	(Country)	(Day/Month/Year Filed)	[] Yes	[] No	
(Number)	(Country)	(Day/Month/Year Filed)	[] Yes	[] No	
or PCT international applicatior subject matter of each of the cla provided by the first paragraph United States Patent and Trader	n(s) designating the Unims of this application of Title 35, United Sonark Office all inforsations, Section 1.56	nited States Code, Section 120, of Jnited States of America that is/ar on is not disclosed in that/those pri- tates Code, Section 112, I acknow mation known to me to be materia which became available between the ing date of this application:	e listed below or application ledge the du il to patentab	w and, insofar a ons in manner ty to disclose to vility as defined	s the
(Application Serial No)	(Filing Date)	(Status) (patented, pending, a	bandoned)		
(Application Serial No.)	(Filing Date)	(Status) (patented, pending, a	bandoned)		

I hereby appoint the following attorneys/agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith and with any divisional, continuation, continuation-in-part, reissue or re-examination application with full power of appointment and substitution of associate attorneys and agents, and to receive all patents which may issue thereon: Thomas E. Beall, Jr., Reg. No. 22,410; Michael J. Colitz, Reg. No. 37,010; Joseph D. Dreher, Reg. No. 37,123; Christopher B. Fagan, Reg. No. 22,987; John X. Garred, Reg. No. 31,830; Michael E. Hudzinski, Reg. No. 34,185; Jeffrey M. Ketchum, Reg. No. 31,174; Richard M. Klein, Reg. No. 33,000; Thomas E. Kocovsky, Jr., Reg. No. 28,383; Sandra M. Koenig, Reg. No. 33,722; John R. Mattingly, Reg. No. 30,293; Scott A. McCollister, Reg. No. 33,961; James W. McKee, Reg. No. 26,482; Richard J. Minnich, Reg. No. 24,175; Jay F. Moldovanyi, Reg. No. 29,678; Philip J. Moy, Reg. No. 31,280; Timothy E. Nauman, Reg. No. 32,283; Sue Ellen Phillips, Reg. No. 32,046; Patrick R. Roche, Reg. No. 29,580; Alan J. Ross, Reg. No. 33,767; Albert P. Sharpe, III, Reg. No. 19,879; Daniel J. Stanger, Reg. No. 32,846; Mark S. Svat, Reg. No. 34,261; John C. Tiernan, Reg. No. 21,078. Address all correspondence to: FAY, SHARPE, BEALL, FAGAN, MINNICH & McKEE

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I declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further, that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18, United States Code, Section 1001, and that such willful false statements may jeonardize the validity of the application or any patent issued thereon.

false statements may jeopardize the validity of the application or any patent issued thereon. 氏名タイプ欄 発明者フルネームサイン 日智道 Satoshi IMASU Date April 3, 1998 Inventor (Typed Name and Signature) Residence Same as Post Office Address Citizenship Japan Post Office Address 2-4-8-305, Shin-machi, Ohme-shi, Tokyo, Japan Ikuo Yoshida Ikuo YOSHIDA April 3, 1998 Inventor (Typed Name and Signature) Residence Same as Post Office Address Post Office Address 5-66-18, Oominami, Musashimurayama-shi, Tokyo, Japan Date April 3, 1998 Inventor (Typed Name and Signature) Residence Same as Post Office Address Post Office Address 2196-5, Hirai, Hinode-machi, Nishitama-gun, Tokyo, Japan (Typed Name and Signature) Residence Same as Post Office Address Post Office Address 3-4-10, Ishigamidai, Ooiso-machi, Naka-qun, Kanagawa, Date April 3, 1998 Inventor Shinobu TAKEURA (Typed Name and Signature) ____Citizenship Japan Residence Same as Post Office Address Post Office Address 375-B-3, Fukuda, Yamato-shi, Kanagawa, Japan (Typed Name and Signature) Citizenship ___ Residence _ Post Office Address